



What Makes a *Good* Grid-Forming Inverter?:

Analytical assessment of Stability and Voltage-Source Behavior.

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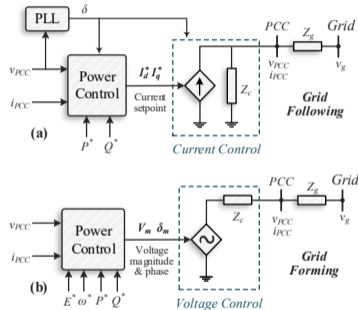
1 - Introduction to Grid Forming Inverters

2 - Problem: Ambiguity in Defining and Quantifying GFM Capabilities

3 - Comparative Analysis of Voltage Control in GFM

4 - Final Remarks

- GFLI/ GFMI inverters differ in their synchronization mechanism



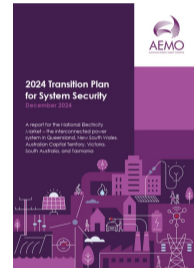
Comparison of GFMI and GFLI

- Fundamentally, a GFM is a **Voltage-Source** behind an **Impedance**

- Market and regulatory incentives are accelerating GFM deployment



Economic: Inertia procurement
(ESO, UK)



Technical: Grid strengthening
(AEMO, AU)

- Yet, GFM capability is still assessed primarily through **qualitative time-domain simulations**.

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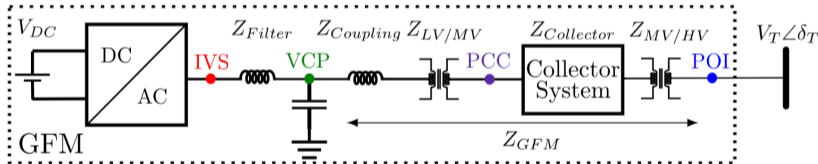
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Where is the internal voltage in a GFM?

- GFM are commonly defined as maintaining a **nearly constant internal voltage phasor (IVP)** over sub-transient to transient time scales (e.g., NESO, NERC, AEMO, UNIFI).
- Unlike Synchronous Machines, the IVP of IBRs is **not physically defined**, but instead realized through control.
- **Most** definitions do not explicitly specify the **location** of the IVP.
- Is the IVP at the **inverter terminal, PoC**, or a **virtual node**?



A generic GFM plant connected to POI [1].

Given that OEMs may implement distinct GFM devices under a common label, this fragmentation necessitates **objective**, **quantitative**, and **comparable** assessment methods.

This motivates the following research questions:

How can different GFM technologies be reliably compared using only black-box models?

Can frequency-domain impedance provide quantitative, technology-agnostic metrics for assessing stability and voltage-source behaviour (VSB)?

What are the system-level implications when a GFM performs well in one metric while degrading others?

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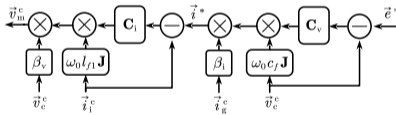
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Three widely adopted voltage-control architectures

PI-Based Voltage Control (PI-VC)

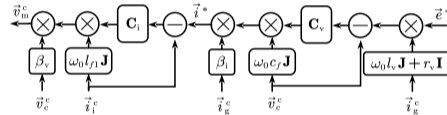
- Classical GFM implementation
- Widely used in research and early industrial designs
- Prone to oscillations in strong grids



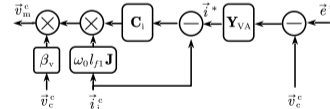
PI-VC — classical voltage control.

Virtual Impedance/ Virtual Admittance Control (VI-VC/ VA-VC)

- Mitigation for Instability in strong grids
- Introduces virtual impedance at the PoC



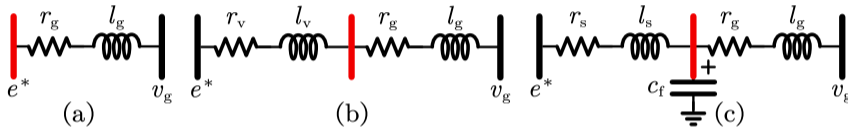
VI-VC — WECC/UNIFI (Tesla, SMA, GE Vernova).



where, $\mathbf{Y}_{VA} = [l_s (s\mathbf{I} + \omega_0 \mathbf{J}) + r_s \mathbf{I}]^{-1}$.

VA-VC — Siemens HVDC, Hitachi GFM- STATCOM.

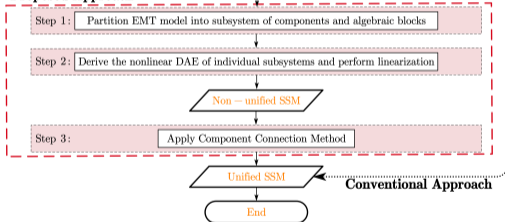
Equivalent Steady-State Circuit Interpretation



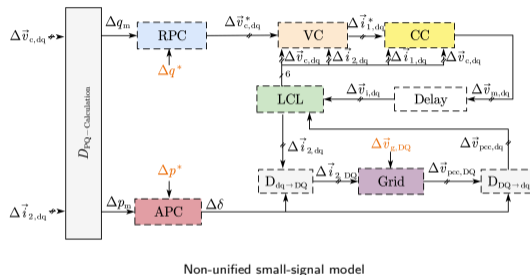
Equivalent steady-state circuit representations of (a) PI-, (b) VI-, and (c) VA-based GFM voltage control, illustrating their impact on **static power-transfer capability** under weak-grid conditions.

While these static differences can be compensated, how do the controllers differ in shaping the *dynamic* behaviour of a GFM?

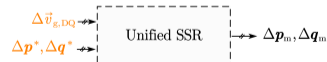
Proposed Approach



Comparing process flowchart of Conventional vs CCM-based modeling.

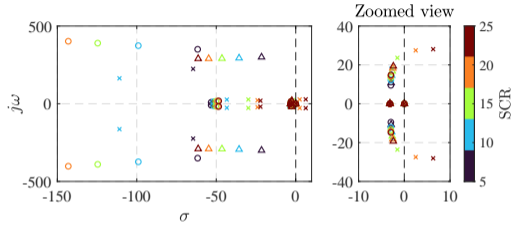


Apply the CCM algorithm:

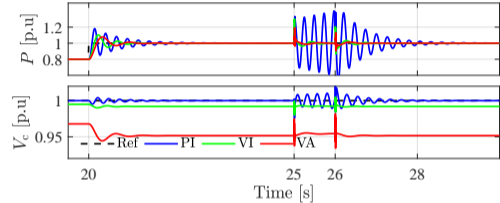


Unified small-signal model using CCM

Stability under varying grid strength: Time Domain



Eigenvalue trajectories as grid strength varies (SCR = 5–25).
Markers: × PI, o VI, Δ VA.



SCR induced instability in the time domain. Δ SCR: 15 → 20 at $t = 25$ s.

PI-based voltage control is a dominant contributor to small-signal instability in strong grids.

Does the virtual impedance introduced for stability compromise the VSB of a GFM, and by how much?

Quantifying Voltage-Source Behaviour

An ideal voltage source will maintain a constant voltage \vec{v} irrespective of current perturbations \vec{i} .

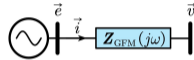
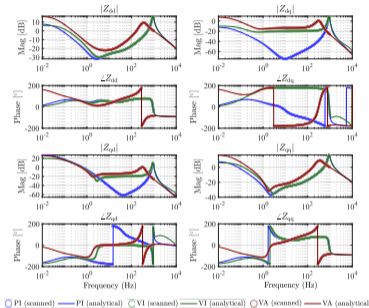


Figure 1. Thévenin Representation of a Voltage Source.

In small-signal form, $\Delta v = -Z_{GFM}(j\omega) \Delta i$ indicating that **ideal VSB** corresponds to $Z_{GFM}(j\omega) = 0$.

While **strong VSB** requires $Z_{GFM}(j\omega)$ to be **small**.



The GFM terminal impedance $Z_{GFM}(j\omega)$ is a **frequency-dependent** 2×2 **matrix in the dq frame**. (Validated between 0.1 to 1000 Hz)

How can “small” be quantified for a frequency-dependent impedance matrix $Z_{GFM}(j\omega)$?

Quantifying VSB using Singular Value Decomposition

In [3], the singular values of the impedance matrix, $\sigma(Z(j\omega))$, are used to quantify VSB.

What does $\sigma(Z(j\omega))$ measure?

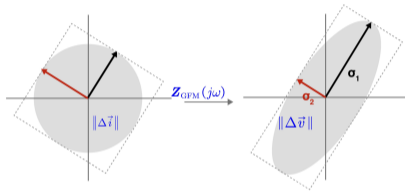


Figure 2. Geometric interpretation of SVD.

The **largest singular value** $\bar{\sigma}(Z_{\text{GFM}}(j\omega))$ gives the *worst-case voltage deviation* due to a unit current perturbation, over **all dq directions**.

Weak grids require technologies (GFM/ Statcom) that lower impedance in the 5–150 Hz band.

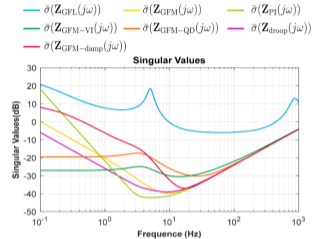


Figure 3. Largest singular value of $Z(j\omega)$: GFL vs GFM [3].

GFL and GFM with identical Q – V droop provide similar *steady-state voltage support*.

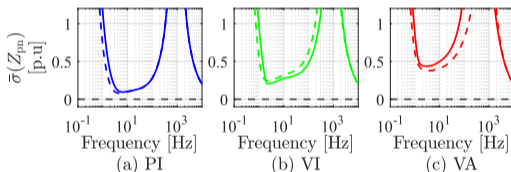
A **small** $\bar{\sigma}(Z(j\omega))$ in 5–150 Hz is required to improve strength at the PoC.

Quantifying Voltage-Source Behaviour of Voltage Control

Design objective. Our objective is to maximise VSB, quantified by minimising the

$$\bar{\sigma}(Z_{\text{GFM}}(j\omega)).$$

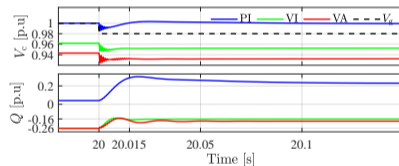
This requires tuning control parameters to **shape the terminal impedance**.



Analysing VSB of VC using the Singular Value Decomposition.

Key observations:

- PI-based VC achieves the **lowest** $\bar{\sigma}(Z_{\text{GFM}})$, and thus the **strongest VSB**.
- VA-VC requires larger virtual impedance than VI-VC in order to stabilize therefore has lower VSB.



Time-domain comparison of PoC voltage and reactive power response to a 2% grid-voltage sag at $t = 20$ s.

Virtual-impedance-based voltage control enhances stability, but at the expense of voltage stiffness.

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Conclusion, Limitation, and Future Work

Conclusion:

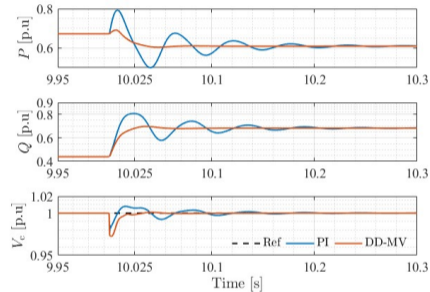
1. A unified CCM-based small-signal framework enables systematic **comparison of GFM control structures**.
2. Stiff VSB can trigger **instabilities near synchronous frequency** in strong-grid conditions.
3. **Frequency-domain impedance** analysis provides a **quantitative basis for assessing** stability and VSB.
4. **Virtual impedance** improves stability but **degrades VSB** and closed-loop performance.

Limitation:

- The work establishes a **framework for comparing stability and VSB** trade-offs.
- It was unable to define a **universal threshold** for what constitutes a **good** grid-forming inverter.

Conclusion, Limitation, and Future Work

- Ongoing work focuses on **voltage-control synthesis** to meet explicit **frequency-domain objectives** for stability and VSB.

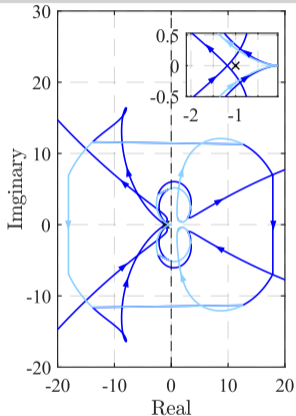


Time-domain comparison between PI-VC and the proposed control in a strong, multi-IBR system.

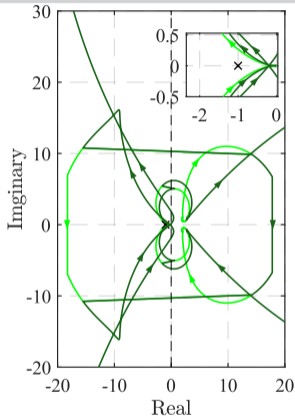
- The proposed framework preserves the **stability benefits of VA-VC** while achieving the **VSB of PI-VC**.

Thank you for your attention.

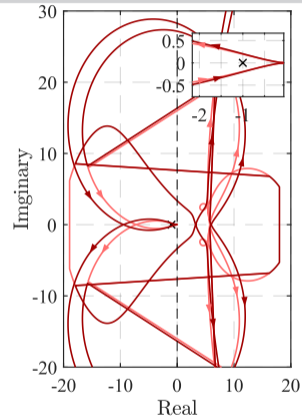
Stability under Varying Grid Strength: Generalized Nyquist Criterion



(a) PI-based VC



(b) VI-based VC



(c) VA-based VC

Stability assessment using the generalized Nyquist criterion (GNC) at SCR = 20.